ABSTRACT OF THE DISCLOSURE

A method and computer program product for
verifying an incremental change to an integrated circuit

5   design are described that include steps of: (a) receiving
as input an integrated circuit design database; (b)
receiving as input an engineering change order; (c)
identifying and marking objects in the integrated circuit
design database to indicate a current state of the

10   integrated circuit design database; (d) applying the
engineering change order to the integrated circuit design
database; (e) analyzing the integrated circuit design
database to generate a list of incremental changes to the
integrated circuit design database resulting from the

15   engineering change order; (f) identifying and marking
objects in the integrated circuit design database
included in the list of incremental changes to
distinguish objects in the integrated circuit design
database that were changed from the current state; and

20   (g) streaming out the integrated circuit design database.

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